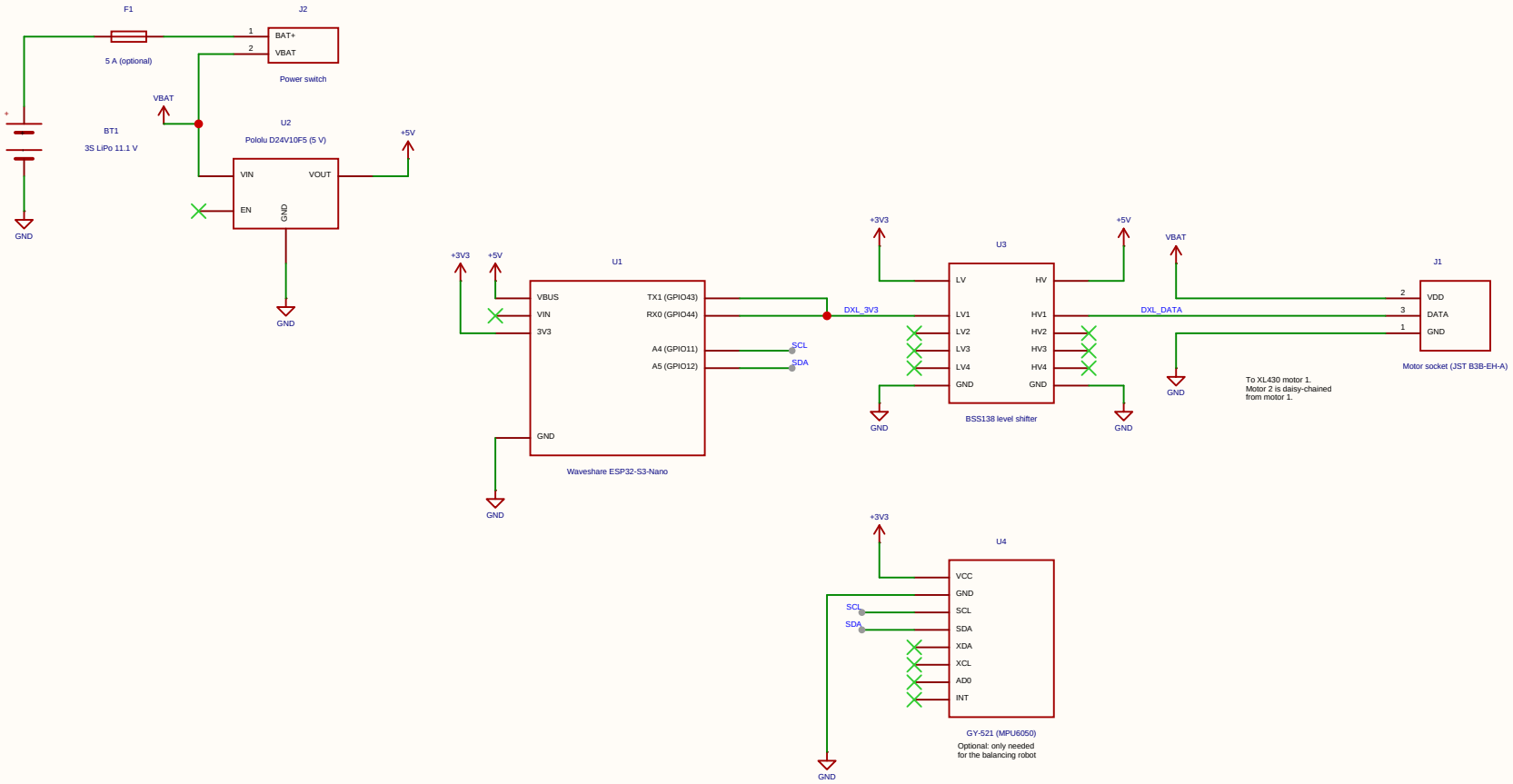



Notes

1. TX1 (GPIO43) is set to open-drain in firmware and jumpered to RX0 (GPIO44).
2. Only level shifter channel 1 is used. Its 10k pull-ups hold the bus high.
3. The motors run straight from VBAT, not from the 5 V regulator.
4. F1 is optional.

| | | |
|---|---|---|
| File : esp32_dxl_board | | |
| ***Title : esp32 UART - DXL TTL Half Duplex*** | | |
| Size : "A4" | Date : Nov 10 | **Rev : 01** |
| | | Id : 2600001 |